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PATENT

JAN 03 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Hiroshi Miyagi

Application No.: 10/520,510

Filing Date: January 4, 2005

For: SEMICONDUCTOR INTEGRATED CIRCUIT AND SEMICONDUCTOR
INTEGRATED CIRCUIT MANUFACTURING METHOD

Confirmation No.: 5960

Group Art Unit: 2812

Examiner: Not Yet Assigned

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PATENT

Error in	Correct data
1. Applicant's name	1.
2. Applicant's address	2.
3. Title	3.
4. Filing Date	4.
5. Application Number	5.
6. Foreign/PCT Application Re:	6.
7. Other: Assignment for Published Patent Application	7. Kabushiki Kaisha Toyota Jidoshokki, Aichi-ken, Japan and Niigata Seimitsu Co., Ltd and Niigata-ken, Japan

Please charge any deficiency or credit any overpayment to Deposit Account No. 23-3050.

Date: January 3, 2006



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Applicant(s)

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Assignment For Published Patent Application

Kabushiki Kaisha Toyota Jidoshokki, Aichi-ken, JAPAN

Niigata Seimitsu Co., Ltd., Niigata-ken, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 23377

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Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/08227 06/27/2003

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Foreign Applications

JAPAN 2002-198928 07/08/2002

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Woodcock; Washburn

Projected Publication Date: To Be Determined - pending completion of Security Review

Non-Publication Request: No

Early Publication Request: No

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Title

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